

# SPE2FH OSCILLATOR



Specifications		
Parameters	Standard	Options
Frequency range	600 MHz to 800 MHz	
Logic output	LVPECL	
Frequency stability		
Overall	+/- 50,0 ppm	+/-20,0 ppm best possible
Initial at 25°C	Please specify	
Temperature		
vs supply voltage change		
vs load change		
Aging first year		
Aging over .... Years		
Operating temperature	0° C ~ +70°C	-20°C ~ +70°C
		-40°C ~ +85°C
		Other on custom request
Storage temperature	-40°C ~ +85°C	-55°C ~ +125°C
Supply voltage	3,3 V +/-5%	
Current	60 mA max	
Load	Vcc-2,0 V into 50 ohm	
Rise/fall time	0,5 ns max	
Duty cycle	45/55 %	
Jitter RMS ( 12kHz ~ 5,0 MHz )	0,1 ps typ , 0,15 ps max.	
Jitter RMS ( 12kHz ~ 20,0 MHz )	0,15 ps typ , 0,20 ps max.	
Jitter RMS ( 50kHz ~ 80,0 MHz )	0,40 ps typ , 0,50 ps max.	
Start-up time	10 ms max.	Other on custom request
Tristate	low or open = enable high = disable	no tristate
Marking	Freq + Code + Week +Year	

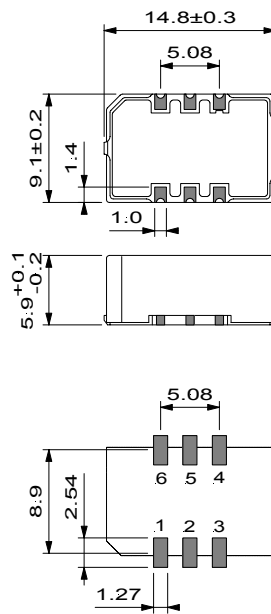
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## Specifications

### Drawing

Package

SP2F 6 pad



Pin configuration

- |                           |
|---------------------------|
| #1: NC                    |
| #2 : E/D                  |
| #3 : GND                  |
| #4 : Output               |
| #5 : Complementary output |
| #6 : Vcc                  |